# Analog Input (note on)

|  |  |  |
| --- | --- | --- |
| **Analog Pin #** | **Header Block** | **Pin** |
| AN0 | J11 | 34 |
| AN1 | J11 | 33 |
| AN2 | J11 | 32 |
| AN3 | J11 | 31 |
| AN4 | J11 | 30 |
| AN5 | J11 | 29 |
| AN6 | J11 | 55 |
| AN7 | J11 | 59 |
| AN8 | J10 | 33 |
| AN9 | J10 | 34 |
| AN10 | J10 | 47 |
| AN11 | J10 | 48 |
| AN12 | J10 | 49 |
| AN13 | J10 | 50 |
| AN14 | J10? | 59? |
| AN15 | J10? | 60? |

# Digital Input (note off)

|  |  |  |
| --- | --- | --- |
| **Digital Pin #** | **Header Block** | **Pin** |
| RG0 | J11 | 6 |
| RG1 | J11 | 5 |
| RG2 | J11 | 35 |
| RG3 | J11 | 37 |
| RG4 | -- |  |
| RG5 | -- |  |
| RG6 | J10 | 23 |
| RG7 | J10 | 24 |
| RG8 | J10 | 25 |
| RG9 | J10 | 26 |
| RG10 | -- |  |
| RG11 | -- |  |
| RG12 | J10 | 3 |
| RG13 | J11 | 4 |
| RG14 | J10 | 4 |
| RG15 | J10 | 1 |

# Parallel Master Port Slave🡪Master

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Slave Pin #** | **Block/Pin** | **Master Pin** | **Block/Pin** | **Function** |
| RE0 | J10/12 | RE0 | J10/12 | PMP Data 0 |
| RE1 | J10/11 | RE1 | J10/11 | PMP Data 1 |
| RE2 | J10/10 | RE2 | J10/10 | PMP Data 2 |
| RE3 | J10/9 | RE3 | J10/9 | PMP Data 3 |
| RE4 | J10/8 | RE4 | J10/8 | PMP Data 4 |
| RE5 | J10/7 | RE5 | J10/7 | PMP Data 5 |
| RE6 | J10/6 | RE6 | J10/6 | PMP Data 6 |
| RE7 | J10/5 | RE7 | J10/5 | PMP Data 7 |
|  |  |  |  |  |
| PMPWR/RD4 | J10/14 | PMWR/RD4 | J10/14 | PMP Control |
| PMPRD/RD5 | J10/13 | PMRD/RD5 | J10/13 | PMP Control |
| IC1/RD8 | J11/25 | RE8/INT1 | J10/37? | Data Ready |
| PMCS2/RD10 | J11/16 | PMCS2/RD10 | J11/16 | PMP Control |
| PMCS1/RD11 | J11/15 | PMCS1/RD11 | J11/15 | PMP Control |
|  |  |  |  |  |